

AMENDMENTS TO THE SPECIFICATION

Please amend the paragraph beginning on page 11, line 15 as follows:

Figure 1 is a block diagram illustrating the structure of a coded signal decoding apparatus according to first to third embodiments of the present invention. Figure 2 is a flowchart for explaining the operation of the coded signal decoding apparatus according to the first embodiment. Figure 3 is a diagram for explaining the structure of a multiplexed signal. Figures 4(a) and 4(b) are diagrams explaining a multiplexed signal in which a packet boundary in a decoding buffer is easily mistaken. Figures 5(a) and 5(b) are diagrams illustrating the relationship between a video packet and video reproduction units. Figures 6(a) and 6(b) are diagrams explaining a padding process for an end part of coded video data by using a formatter. Figure 7 is a diagram illustrating a unique code sequence inserted in coded and multiplexed data. Figure 8 is a block diagram illustrating a modification of a coded signal decoding apparatus according to first to third embodiments of the invention. Figure 9 is a block diagram illustrating the conceptual structure of a coded signal decoding apparatus according to the present invention. Figures ~~10(a)-10(c)~~ 10(a)-10(e) explain the operation of the coded signal decoding apparatus of the present invention in the case where an input packet is a video packet, having emphasis on a formatter and a start code state storage unit. Figures 11(a)-11(d) further explain the operation of the coded signal decoding apparatus of the present invention in the case where an input packet is a video packet, having emphasis on a formatter and a start code state storage unit. Figure 12 is a diagram explaining coded video data reading operation by a conventional coded signal reproduction apparatus.